

# DELTA TAU PMAC MOTION CONTROL CIRCUIT CARD

+5 V, 1.5A  
± 12 V, 0.25 A

PC BUSS

P1 & P2  
BUSS CONTROL

INTERRUPT CONTROL

MEZZININE  
CPU  
CARD

CPU  
(24 BIT DSP)

ROM  
(128K x 24)

RAM  
(128K x 24)

EEROM  
(2K x 8)

WATCH  
DOG  
TIMER

REG-  
ULATOR ± 5V

OPTO  
ISOLATION

DIFFERENTIAL  
RECEIVER

VOLTAGE TO  
FREQUENCY  
CONVERTER

DSP  
GATE  
1

AMP ENABLE

OPTO  
ISOLATION

DAC

DAC

DAC

DAC

P/O  
J8  
(  
MACHINE 1)

P/O  
J8  
(  
MACHINE 1)

± 12V  
LIMITS  
HOME  
AMP  
FAULT  
ENC

P/O  
J2  
(  
JPAN)

JS I/O 1

P/O JS I/O 1

OPTION

REG-  
ULATOR ± 5V

OPTO  
ISOLATION

DIFFERENTIAL  
RECEIVER

VOLTAGE TO  
FREQUENCY  
CONVERTER

DSP  
GATE  
1

AMP ENABLE

OPTO  
ISOLATION

DAC

DAC

DAC

P/O  
J8  
(  
MACHINE 1)

P/O  
J8  
(  
MACHINE 1)

P/O  
J2  
(  
JPAN)

JS I/O 1

P/O JS I/O 1

DIGITAL  
I/O

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**BLOCK DIAGRAM,  
MILLING MACHINE**

SIZE	DRAWING NUMBER	REV:
A	BLOCK1	3
SCALE: 1:1 FILE: BLOCK1.DWG		SHEET 1 OF 1

ENC MEANS ENCODERS  
I/O MEANS INPUT AND/OR OUTPUT  
P/O MEANS PART OFF