

DELTA TAU PMAC 2 MOTION CONTROL CIRCUIT CARD

+5 V, 1.5A
± 12 V, 0.25 A

PC BUSS

TB1
POWER IN
(FOR BUSLESS OP)

P1 & P2
BUSS CONTROL

J5
SERIAL PORT

MEZZININE
CPU
CARD

CPU
(24 BIT DSP)

P3

INTERRUPT
CONTROL

WATCH
DOG
TIMER

ROM
(128K × 24)

RAM
(128K × 24)

EEROM
(2K × 8)

P4

TB2
WATCHDOG
OUT

OPTION
JRS422/DB9
RS-422 INTER

OPTION
JRS422/26PIN
RS-422 INTER

P/O
J9/10
(
MACHINE 1)

± 12V

REG-
ULATOR

± 5V

LIMITS

OPTO
ISOLATION

HOME

AMP FAULT

ENC

DIFFERENTIAL
RECEIVER

DSP
GATE
1

P/O
J9
(
MACHINE 1)

+5 V POWER
WATCHDOG TTL
ADC CONTROL
DAC OUTPUTS
AMP ENABLE

P/O
J10
(
MACHINE 2)

+5 V POWER
WATCHDOG TTL
ADC CONTROL
DAC OUTPUTS
AMP ENABLE

J8
POS COMP

OPTION

P/O
J11/12
(
MACHINE 1)

± 12V

REG-
ULATOR

± 5V

LIMITS

OPTO
ISOLATION

HOME

AMP FAULT

ENC

DIFFERENTIAL
RECEIVER

DSP
GATE
1

P/O
J11
(
MACHINE 3)

+5 V POWER
WATCHDOG TTL
ADC CONTROL
DAC OUTPUTS
AMP ENABLE

P/O
J12
(
MACHINE 4)

+5 V POWER
WATCHDOG TTL
ADC CONTROL
DAC OUTPUTS
AMP ENABLE

OPTION
J1
ANALOG IN

J2
THUMBWHEEL

DIGITAL
I/O

J4
MACRO NET

J3
DIGITAL I/O

J7
HANDWHEEL

J6
DISPLAY

ENC MEANS ENCODERS
I/O MEANS INPUT AND/OR OUTPUT
P/O MEANS PART OFF

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BLOCK DIAGRAM,
MILLING MACHINE

SIZE A	DRAWING NUMBER BLOCK2	REV. 2
SCALE 1 : 1		FILE: BL0CK1.DWG
		SHEET 1 OF 2